

<b>Notice of References Cited</b>	Application/Control No. 10/816,103		Applicant(s)/Patent Under Reexamination GROCHOWSKI ET AL.	
	Examiner Robert E. Fennema		Art Unit 2183	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,092,175	07-2000	Levy et al.	712/23
*	B	US-6,223,208	04-2001	Kiefer et al.	718/108
*	C	US-6,378,067	04-2002	Golliver et al.	712/244
*	D	US-5,485,626	01-1996	Lawlor et al.	719/315
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Foldoc.org. "virtual memory, privileged instruction, supervisor mode, process, multithreading, and context switch". 1995-2002.
	V	Patterson, David and Hennessy, John. "Computer Architecture: A Quantitative Approach". Morgan Kaufmann Publishers, Third Edition. May 17, 2002. Pages 215-219.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.